# CPRE 492 WEEKLY REPORT MAY15-25 Report 7

**Advisor:** Lee Harker

**Client:** Lee Harker / Department of Electrical and Computer Engineering

**Project Title:** CyLocker Access System

#### **Group Roles**

Team Leader -> Nathan Castek
Team WebMaster -> Corey Coazzato
Team Communicator -> Nathan Lafferty
Team Key Concept -> Priyank Patel

Team Technical Leader -> Mohammad Syazwan

#### **Report Summary**

Coming very close to finalizing MCU needs a lot of work put in to finalize design LCU design is finishing

#### **Accomplishments**

Priyank started his coursework Raspberry Pi Model 2b+ development started LCU PCB changes

#### **Meeting Minutes**

**Date:** 3/3/15

**Duration:** 15 minutes

Attendance:

Nathan Castek Nathan Lafferty Mohammad Syazwan Priyank Patel

Corey Coazzato

#### **Summary:**

Discussed last week's accomplishments Discussed goals for next week

Spring Break is in two weeks, need to be done with final designs by then (self-imposed deadline)

## **Progress Impediments**

### **Individual Contributions** (Name) (this report, total) Castek: 6,26 LCU Case Picked (1.5) Currents measured for new power consumption numbers (1.5) PCB design configured to fit in case (1) Measurements for holes in PCB made (2) , 10 Corey: Lafferty: , 14 • Started coding python libraries (5) o Data Access Layer Locker object User object **Priyank:** 3, 18.0 Finished training class (2) o did some practice with software • Helped with PCB design (1) Mohammad: 3, 19 • measured holes for PCB (1) designed PCB to fit in case (1) updated components of PCB with the surface mount parts (1) **Plans** Corey: Lafferty: **Everyone Priyank:** Muhammad:

Castek: