

CPRE 491 WEEKLY REPORT

MAY15-25

Week 8 Report

Advisor: Lee Harker

Client: Lee Harkin / Department of Electrical and Computer Engineering

Project Title: CyLocker Access System

Group Roles

Team Leader ->	Nathan Castek
Team WebMaster ->	Corey Coazzato
Team Communicator ->	Nathan Lafferty
Team Key Concept ->	Priyank Patel
Team Technical Leader ->	Mohammad Syazwan

Weekly Summary

Design Document was given significant focus.

Further integration testing w/ power systems + atmega, and atmega + servo

Weekly Accomplishments

Further integration testing was completed.

Meeting Minutes

Date: 10/22/2014

Duration: 60 minutes

Attendance:

Nathan Lafferty
Mohammad Syazwan
Priyank Patel
Corey Coazzato

Missing:

Nathan Castek - out of town

Summary:

Discussed previous week's accomplishments.

Discussed planned next week's accomplishments.

Emphasized that this week will be spent working on the design doc, and each person will need to put in 4-8 hours to complete it.

Minutes:

<https://docs.google.com/a/iastate.edu/document/d/1fu-UVoEegXOKp-DFKWsdBlJ30QgI5zWzD82Axs0O4/edit>

Progress Impediments

Design Doc needs to be finished

Individual Contributions

(Name) (this week, total)

Castek: 5, (36.4)

- Integration testing with ATMEGA and Servo (1.5)
- Design Document Parts for Lock, LED, Servo, and interconnections (2)
- Created schematics for interconnection circuitry (1)
- Talked to client about integration testing issues (0.5)

Corey: 5, (37.5)

- Website updates- 0.5 hours
- XBee range testing- 0.5 hour
- Team meeting - 1 hour
- arduino with servo circuit testing- 1 hour
- design doc - 2 hour

Lafferty: 6, (41)

- Attended Meeting (1)
- Weekly Report (1)
- Design Doc (3)
- Read card reader from Pi (0.5)
 - Standard IO
- Toyed with LCD Display (0.5)

Priyank: 5, (36)

- Design Document Parts (2)
- testing with ATMEGA PSpice (2)
- Team meeting (1)

Mohammad: 5, (37.5)

- attended meeting - 1.0 hour
- worked on design document - 3 hours
- worked on schematic circuit of locker unit using Eagle software - 1 hour

Weekly Plans

Mohammad

- complete designing the schematic circuit of locker unit

Priyank

- do complete circuitry to power up ATMEGA

Castek

- Solve issues with integration testing w/servo
- Explore the bi-colored LED
- Go over circuits with client and further research to make sure they are sufficient

Corey

- Finishing touches on design doc
- further circuit testing

Lafferty

- Finishing touches on design document
- Write simple scripts for Pi
- Flesh out use-logic for Pi